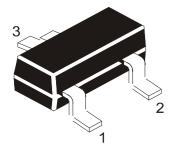
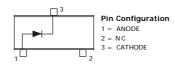


# SILICON PLANAR SWITCHING DIODE





## **CMBD4448**

SOT-23 Formed SMD Package

Marking CMBD4448 - CA2

# For General Purpose Switching Applications

### **ABSOLUTE MAXIMUM RATINGS**

DESCRIPTION	SYMBOL	VALUE	UNIT	
Non Repetitive Peak Reverse Voltage	V <sub>RM</sub>	100	V	
Peak Repetitive Reverse Voltage	V <sub>R</sub>	75	V	
Forward Continuous Current	*I <sub>FM</sub>	500	mA	
Average Rectified Output Current	*I <sub>O</sub>	250	mA	
Non Repetitive Peak Forward Surge Current @ t=1ms	I <sub>FSM</sub>	4	А	
Non Repetitive Peak Forward Surge	I <sub>FSM</sub>	2	A	
Current @ t=1s	'FSM	-		
Power Dissipation @ T <sub>a</sub> =25 <sup>o</sup> C	*P <sub>D</sub>	350	mW	
Operating and Storage Junction Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	- 65 to + 150	°C	

## THERMAL RESISTANCE

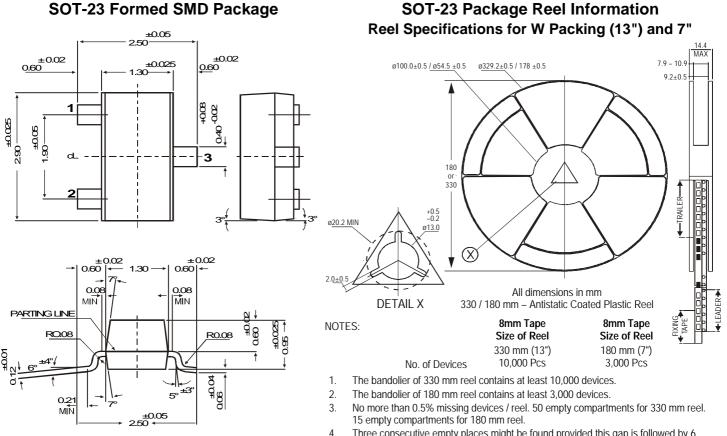
Junction to Ambient in free air *R <sub>th (j-a)</sub> 357 K/W
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\* Valid provided that terminals are kept at ambient temperature

## ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

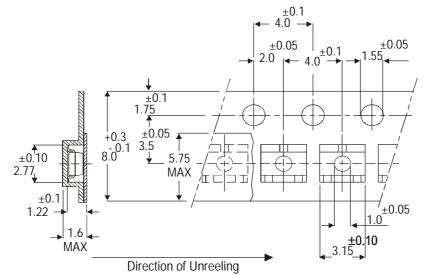
DESCRIPTION	SYMBOL	<b>TEST CONDITION</b>	MIN	MAX	UNIT
Forward Voltage	V <sub>F</sub>	$I_F = 5 \text{ mA}$	0.62	0.720	V
		I <sub>F</sub> = 10 mA		0.855	V
		I <sub>F</sub> = 100 mA		1.0	V
		I <sub>F</sub> = 150 mA		1.250	V
Reverse Voltage Leakage Current	I <sub>R</sub>	V <sub>R</sub> =75V		2.5	μΑ
		V <sub>R</sub> =75V, T <sub>j</sub> =150°C		50	μΑ
		V <sub>R</sub> =25V, T <sub>j</sub> =150°C		30	μA
		V <sub>R</sub> =20V		25	nA
Diode Capacitance	C <sub>d</sub>	V <sub>R</sub> =0V, f=1MHz		4.0	pF
Reverse Recovery Time	t <sub>rr</sub>	$I_F=I_R=10$ mA, $I_{rr}=0.1$ x $I_R$ , $R_L=100\Omega$		4.0	ns

# **SOT-23** Formed SMD Package



- Three consecutive empty places might be found provided this gap is followed by 6 4. consecutive devices.
- The carrier tape (leader) starts with at least 75 empty positions (equivalent to 330 mm). 5. In order to fix the carrier tape a self adhesive tape of 20 to 50 mm is applied. At the end of the bandolier at least 40 empty positions (equivalent to 160 mm) are there.

## **Tape Specification for SOT-23 Surface Mount Device**



#### Packing Detail

Packing De	cking Detail All dimensions in mm						
PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Oty	Size	Qty	Size	Qty	Gr Wt
SOT-23 T&R	3K/reel	136 gm/3K pcs	3" x 7.5" x 7.5"	12 K	17" x 15" x 13.5"	192 K	12 kgs
			9" x 9" x 9"	51 K	19" x 19" x 19"	408 K	28 kgs
	10K/reel	415 gm/10K pcs	13" x 13" x 0.5"	10 K	17" x 15" x 13.5"	300 K	16 kgs

Continental Device India Limited

### CMBD4448

SOT-23 Formed SMD Package

## Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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